

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | | | |
|-------------|--|---|-----------|--------------------|
| Applicant: | Chow, Lap-Wai; et al. |) | Examiner: | Lee, Eugene |
| | |) | | |
| Serial No.: | 10/735,841 |) | Art Unit: | 2815 |
| | |) | | |
| Filed: | December 12, 2003 |) | Our Ref: | B-4425NP 621267-1 |
| | |) | | |
| For: | "Integrated circuit modification using well implants" |) | Date: | September 10, 2007 |
| | |) | | |
| | |) | Re: | <i>Response</i> |
| | |) | | |

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is in reply to the non-final Office Action mailed on June 8, 2007, an initial response to which is initially due by

Monday, September 10, 2007.

Please consider the following remarks. **All remarks herein are made without prejudice.**

Remarks/Arguments begin on page 2 of this paper.